

Dicipline:	Electrical Engineering	Semester:	5th	Name of the Teaching Faculty:	ER. Kishore Kumar Soren
Subject:	Digital Electronics and Microprocessor	No of Days/Week Class Allotted:	5 day	Semester Start Date:	15 Sep 2022
			No. of Weeks:		

WEEK	Class Day	Theory Topics
1st week 15/9/22 to 17/9/22	15.09.22 1st Thursday	Basic of Digital Electronics :- Binary, octal, decimal and hexadecimal number conversion from one number to another number.
	17.09.22 2nd Saturday	Binary addition, subtraction, multiplication and division, 1's Complement and 2's Complement
	3rd	
	4th	
	5th	
2nd week 19/09/22 to 24/09/22	19/09/22 1st Monday	Subtraction of binary numbers in 2's Complement method.
	20.09.22 2nd Tuesday	Use of weighted and un-weighted code and write binary equivalent number for a number in 8421, Ex-3, Gray code and viceversa.
	21.09.22 3rd Wednesday	Importance of Parity bit.
	22.09.22 4th Thursday	Logic gate: AND, OR, NOT, NAND, NOR, EX-OR and EX-NOR gate with truth table.
	24.09.22 5th Saturday	Realize AND, OR, NOT operation using NAND, NOR gate.
3rd week 26/09/22 to 1/10/22	26.09.22 1st Monday	Different Postulates and De-morgan's theorem in boolean algebra.
	27.09.22 2nd Tuesday	Use of boolean algebra for simplification of logic expression.
	28.09.22 3rd Wednesday	Karnaugh map for 2, 3, 4 variable logical expression.
	29.09.22 4th Thursday	Simplification of SOP logic expression using Karnaugh map.
	1.10.22 5th Saturday	Simplification of POS logic expression using Karnaugh map.



WEEK	Class D	Theory Topics
4th week 10/10/22 to 15/10/22	10.10.22 Monday	Combination Logic Circuit: Give the Concept of Combinational Logic Circuits.
	11.10.22 Tuesday	Verify the half adder circuit and verify its functionality using truth table.
	12.10.22 Wednesday	Realize half adder using NAND gate only.
	13.10.22 Thursday	Realize half adder using NOR gate only.
	14.10.22 Friday	Explain Full adder circuit and its operation with truth table.
	15.10.22 Saturday	Realize full-adder using two Half-adder and its truth table.
5th week 17/10/22 to 22/10/22	17.10.22 Monday	Realize Full adder using OR gate and write its truth table.
	18.10.22 Tuesday	Explain Full Subtractor circuit and its operation with truth table.
	19.10.22 Wednesday	Operation of $4 \times 1$ Multiplexers and its truth table.
	20.10.22 Thursday	Operation of $1 \times 4$ Demultiplexers and its truth table.
	21.10.22 Friday	Working of binary to decimal encoders and its truth table.
	22.10.22 Saturday	Due to Divan
6th week 24/10/22 to 29/10/22	24.10.22 Monday	Working of $3 \times 8$ decoder circuit and two bit magnitude Comparator circuit.
	25.10.22 Tuesday	Realize higher order mux into lower order mux and its truth table.
	26.10.22 Wednesday	Realize higher order DEMUX into lower order DEMUX and its truth table.
	27.10.22 Thursday	
28.10.22 Friday		
29.10.22 Saturday		



# ARYAN SCHOOL OF ENGINEERING & TECHNOLOGY

Discipline:	Electrical Engineering	Semester:	5th	Name of the Teaching Faculty	ER. KISHORA KUMAR SASANA
Subject:	Digital Electronics and Microprocessor.	No of Days/Week Class Allotted:	5 day	Semester From date:	15 sept To date _____
				No. of Weeks:	

WEEK	Class Day	Theory Topics
7th week 31/10/22 to 5/11/22	31.10.22 1st Monday	Sequential Logic Circuits: Give the idea of sequential logic circuit.
	1.11.22 2nd Tuesday	State the necessity of clock and give the concept of level clocking and edge triggering.
	2.11.22 3rd Wednesday	Clocked SR flip flop with preset and clear inputs.
	3.11.22 4th Thursday	Construction level clocked J-K flip flop using S-R flip flop and explain its truth table.
	5.11.22 5th Saturday	Study of master slave J-K flip flop and its race around condition.
8th week 7/11/22 to 12/11/22	7.11.22 1st Monday	Give the truth tables of edge triggered D and T flip flops and draw their symbols.
	8.11.22 2nd Tuesday	Application of flip flops.
	9.11.22 3rd Wednesday	Define modulus of a counter.
	10.11.22 4th Thursday	U-nit asynchronous counter and its timing diagram.
	12.11.22 5th Saturday	Asynchronous decade counter.
9th week 14/11/22 to 19/11/22	14.11.22 1st Monday	U-nit synchronous counter.
	15.11.22 2nd Tuesday	Differentiate between synchronous counter and asynchronous counter.
	16.11.22 3rd Wednesday	State the need for a register and list all the four types of registers.
	17.11.22 4th Thursday	Working of SISO, SIPO register with truth table using flip flop.
	19.11.22 5th Saturday	Working of PISO, PIPO register with truth table using flip flop.



WEEK	Class Day	Theory Topics
10th week 21/11/22 to 26/11/22	21.11.22 1st Monday	8085 microprocessor :- Introduction to microprocessors.
	22.11.22 2nd Tuesday	Introduction to microcontroller.
	23.11.22 3rd Wednesday	Architecture of intel 8085A microprocessor and description of each block.
	24.11.22 4th Thursday	8085 Pin diagram and it's description.
	26.11.22 5th Saturday	Define Stack, stack pointer and stack top.
	11th week 28/11/22 to 3/12/22	28.11.22 1st Monday
29.11.22 2nd Tuesday		Opcode and operand in 8085.
30.11.22 3rd Wednesday		Differentiate between one byte, two byte and three byte instruction with examples.
1.12.22 4th Thursday		Instruction set of 8085 - example.
3.12.22 5th Saturday		Addressing mode of 8085.
12th week 5/12/22 to 10/12/22	5.12.22 1st Monday	Fetch cycle and machine cycle of 8085.
	6.12.22 2nd Tuesday	Instruction cycle and T-state of 8085.
	7.12.22 3rd Wednesday	Timing diagram for memory read.
	8.12.22 4th Thursday	Timing diagram for memory write.
	10.12.22 5th Saturday	Timing diagram for I/O read.



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WEEK	Class Day	Theory Topics
13th week 12/12/22 to 17/12/22	12.12.22 1st Monday	Timing diagram for I/O write.
	13.12.22 2nd Tuesday	Timing diagram for 8085 instruction.
	14.12.22 3rd Wednesday	Counter and time delay.
	15.12.22 4th Thursday	Simple assembly language programming of 8085
	17.12.22 5th Saturday	Differentiate between microprocessor and microcontroller.
14th week 19/12/22 to 22/12/22	19.12.22 1st Monday	Interfacing and support chips :- basic interfacing concept.
	20.12.22 2nd Tuesday	memory mapping.
	21.12.22 3rd Wednesday	I/O mapping.
	22.12.22 4th Thursday	Functional block diagrams and description of each block of programmable peripheral interface.
	5th Saturday	Application of 8255.
15th week	1st Monday	Seven Segment LED display
	2nd Tuesday	Square wave generator.
	3rd Wednesday	Traffic light controller.
	4th Thursday	class test -1
	5th Saturday	class test -2